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Article

Electrothermal Multicriteria Comparative Analysis of Two Competitive Powertrains Applied to a Two Front Wheel Driven Electric Vehicle during Extreme Regenerative Braking Operations

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Abstract: The powertrain performance in an electric vehicle is fully dependent on the electrical and thermal constraints of the static converters ensuring the power transfer taking place between the energy storage systems and the electromechanical machines. These constraints depend on the architectures of the power converters, and their control strategies. Particularly, the maximal limits are reached in maneuvers such as hard regenerative braking circumstances. Indeed, braking recovery is a critical phase in the vehicle's operation, and its duration and intensity may strongly impact the vehicle's battery behavior or integrated hybrid storage system. The innovative objective of the paper is to propose an electrothermal multicriteria comparative study based on electrical and thermal criteria for two competitive powertrains. These semi-active power configurations (a 3-level DC/DC converter-based, and a Z-source converter-based) are implemented in a two-front wheel driven electric vehicle during extreme regenerative braking conditions. Open-loop and closed-loop controls were implemented in the Z-source using the maximal constant boost control with 3rd harmonic injection modulation technique. We considered two paralleled IGBT modules instead of the single shoot-through structure. Our approach is based on simulation during an extreme braking maneuver leading to heavy repercussions on the overall powertrain system. The aim is to investigate the challenging structure of the Z-source. Results showed that the proposed 3-level DC/DC-based topology has better performances in terms of power losses, efficiency, thermal behavior, and electromagnetic interference.

Keywords: hybrid energy storage system; electric vehicle; 3-level DC/DC converter; Z-source converter; electrothermal behavior



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1. Introduction

1.1. General

Several reasons have led to the evolution and the emergence of electric vehicles (EV). Negative environmental impacts of ICE-based vehicles, reductions in noise levels, human health concerns, and safety improvements are directing organizations, legislators, and governments to impose a series of standards and laws encouraging the use of zero-emission vehicles. Regulations and agreements have accelerated the process of transport electrification for carmakers. In terms of market share, the electric vehicle and hybrid electric vehicle markets promise an average of EUR 250 billion in Europe itself, by 2025. This has launched a wide-open competition between car manufacturers, each claiming a big part of this promising market which guarantees a solid transformation of transportation technology toward electromobility.

Transportation electrification is a necessity at strategic levels: energy independence, environmental and health constraints, economic sustainability, presence in the transportation market . . . and surely research and development.

In terms of electromechanical powertrain, the technological aspects of an EV are mainly based on energy storage systems, drivetrain systems, and power electronics converters. Hybridization of the energy storage system consists of integration of several energy sources. The energy management system of the hybrid energy storage system (HESS) efficiency depends on the strategy adopted. It considers the advantages of the secondary energy storage element (taken as a high-power density device) from the perspective of improving the primary energy storage lifespan (taken as a high-energy density element). The power conversion and transfer between different DC voltage energy storages and dynamic electromechanical converters (electric motors) are controlled by power electronic based static converters. Selection of static and dynamic converters and their related controls highly depends on many factors, such as topologies, structures, and technologies, but also, and mainly, on high transient and severe operations.

The aim of the paper is to propose an electrothermal multicriteria comparative study of two competitive conversion powertrains applied to a two-front wheel driven electric vehicle during extreme regenerative braking operations. Regenerative braking consists of transferring a part of the kinetic energy stored in the vehicle mass to the hybrid storage system in the powertrain [1,2]. During these severe transfer process, power converters undergo peak power surges, involving generation of high amplitude transient electrical signals of large values and inducing critical operating constraints on the various elements.

The simulation represents the electrothermal behavior of the whole powertrain. The aim will be to analyze the electrical transients and the various states characterizing two different hybrid energy storage systems in order to compare and validate the behavior of the various power conversion architectures and control structures associated without exceeding the electrical constraints of different elements including power switches.

In this paper, a comparison is conducted between a Z-source-based converter and a 3-level converter/SVPWM inverter (called 3-level/SVPWM) applied to an HESS consisting of a Li-ion battery as a primary source and an ultracapacitor (UC) as a secondary source. In literature, until now, such a comparative approach between the two competitive topologies for EV application and impact on the HESS has not been mentioned.

1.2. State of the Art

The Z-source inverter (ZSI), introduced in [3], was proposed to combine the functions of the DC/DC converter and the voltage source inverter. In a conventional voltage source inverter, the output voltage is always lower than the DC input voltage and the converter provides buck DC/AC power conversion. An additional boost converter is then required resulting in a two-stage power conversion system having drawbacks on the system cost, complexity, and efficiency.

The ZSI has both buck and boost abilities. It applies an impedance network to interfere between the power source and the inverter of the system [4,5]. It boosts the DC bus voltage of an inverter by using the shoot-through (ST) state. One of the advantages is that it does not require dead-time control to protect the voltage source against the same phase leg inverter switches during short-circuit. Several modulation methods can be applied to the inverter [6]. Authors in [7] studied the comparison between a modified space vector pulse width modulation (SVPWM) and the simple boost control (SBC) modulation method. The simulation results show superiority of the proposed modified SVPWM in terms of harmonic spectra distribution and power efficiency. In the same perspective, authors in [8] obtained a better voltage THD factor by proposing a modified modulation shoot-through strategy. Authors in [9] simulated and analyzed the boost factor, voltage gain, duty cycle, and voltage stress across the switches for the SBC and the maximum boost control methods. They showed that better performance would be obtained if modulation index and shoot-through duty cycle were set to a high value. A modified simple boost control with independence relation between modulation index and shoot-through duty has been simulated and analyzed in [10]. The simulation shows that the modified simple boost control, as well as the maximum boost control could utilize a high modulation index

to generate output voltage that requires high voltage gain. Nevertheless, the coupling between the shoot-through duty cycle and the modulation index makes a tradeoff between the voltage gain and the output voltage harmonic [11]. A modified discontinuous PWM technique was also proposed for a three-phase ZSI offering smooth voltage gain variation, a wide operating range, reduced voltage stress, and improved total harmonic distortion [12].

Z-source applications may reach several domains as microgrids applications [13], fuel cell applications [14], and renewable energy applications [15]. For motor traction applications, by controlling the shoot-through duty cycle, the Z-source provides greater output AC voltages, ride-through capability under voltage sags, current line harmonics reduction, and output voltage range extension. The latter point allows for increasing power capacity to the motor and thus minimizing its ratings. These assumptions were supported in [16]. For EV applications, the paper in [17] proposed a small signal modeling and a voltage controller design of a bidirectional quasi-inverter (BqZSI) to be connected to the battery of an electric vehicle. A decoupled control algorithm between the DC side and the AC side of the BqZSI is achieved. The authors in [18] developed an improved control strategy for a quasi-Z-source inverter (qZSI)-fed permanent-magnet synchronous machine (PMSM)-based electric vehicle. The controller with feed forward compensation has proven its stabilizing effectiveness against disturbance during non-shoot through state [19].

The traditional Z-source inverter has a higher capacitor voltage of the capacitor and a startup shock. Researchers proposed a variety of improved Z-source topologies by increasing boost capacity or reducing capacitor voltage. In [20], a simulation based on eleven Z-source topologies analyzed in terms of boost capacity, inductance start-up current, capacitance stress, and economy was performed. Two kinds of promising topologies were suggested. Other authors worked on minimizing current ripples [21], and improving converter efficiency [22] and reducing output voltage distortions [23].

Propositions for a UC-based hybrid energy storage system for an electric vehicle were elaborated [24–26]. The objectives are to extend the lifetime of the battery by optimizing the energy management strategy. Some authors have simulated the power distribution strategy on the Pontryagin's minimum principle [27], others on the genetic algorithm optimization technique [28]. The authors in [29] applied a bidirectional buck-boost DC-DC resonant converter to a kinetic energy recovery system to manage the UC pack of an electric vehicle in order to meet the dynamical tests of the Formula SAE electric competition.

When using qZSI and BqZSI, an enhanced frequency dividing coordinated control strategy is established to optimize the dynamic power regulation and the battery current stress in short timescale [30]. The asymmetric characteristic related to the uneven power distribution was discussed in [31]. In [32], a distributed energy storage design battery/UC within an electric vehicle for smarter mobility applications was introduced. Loss and the efficiency analysis of the bi-directional interleaved converter was presented.

1.3. Comparative Study between Z-Source and Classical Topologies

Comparison between a bidirectional Z-source and a two-stage design were conducted in [33]. The improved modulation techniques are based on the conventional sinusoidal PWM or the space vector PWM. These techniques can be addressed from different perspectives including processing speed, ease of digital implementation, mathematical simplification, or improvement of harmonics distortion. A novel PWM control strategy for a DC linked quasi-switched boost inverters was deployed in [34] for low voltage gain application improving the voltage and reducing the conduction loss and the inductor current ripple. Comparison has been established between a qZSI and a two-stage inverter.

A study in [35] compares a three-phase three-level voltage source inverter with an intermediate DC/DC boost converter and a quasi-Z-source inverter in terms of passive element values and dimensions, semiconductor stresses, and overall efficiency. The simulation tests and experimental set-up was performant for 1 kW, 50 Hz fundamental frequency.

Power loss comparison of a Z-source inverter and a voltage-source inverter linked to a DC/DC-boost converter was also simulated in [36] for a wind turbine application

giving superiority to the DC-DC converter/inverter topology in terms of efficiency. For PV applications, a comparison of a qZSI and a traditional two-stage inverter shows lower THD and higher efficiency for the qZSI [37].

The contribution of this work is listed below:

- Comparative study based on electrical and thermal criteria for two competitive conversion powertrains: based on a 3-level DC/DC converter, and based on a Z-source converter.
- Open-loop and closed-loop control strategies have been implemented in the Z-source using the maximal constant boost control with the 3rd harmonic injection modulation technique.
- The two considered structures are implemented in a two-front wheel electric vehicle during extreme regenerative braking operations in simulation environment.
- Analysis of the different simulation results have been performed based on the cited criteria.

1.4. System Description

The system consists of a, two in-wheel interior permanent magnet synchronous motors (IPMSM), front driven electric vehicle. Each motor has an output power of 30 kW. The motors are supplied with a hybrid energy storage system which includes a Li-ion battery and an ultracapacitor (UC). The battery as a primary energy source delivers the main energy to the vehicle in order to ensure the drive of the vehicle. Whereas the role of the UC, as a high-power density element, is to deliver/recover energy during high peak power demand, hence protecting the battery from excessive currents and overvoltage while assuring a maximum braking capacity and a high-efficiency behavior. One goal is to minimize the intervention of the Li-ion battery, which prevents many weaknesses relative to the electrochemical processes taking place during charging and discharging phases. These processes lead to a loss of capacitance, an increase in its internal resistance, a reduction in its lifespan, and, in extreme cases, thermal runaway with risk of fire. These consequences accelerate during high transient solicitations. During these severe operations, power converters experience peak power surges, involving generation of transient signals and inducing critical operating constraints for the various power elements.

During braking, the nature and form of the electromechanical input power, coming from the wheels, is handled by DC/AC and DC/DC converters ensuring proper torque and DC bus voltage controls. This energy is adapted to be stored in the energy storage elements. During acceleration, the power flow is reversed. The topology used in the present paper is a semi-active topology. The UC is connected to the regulated DC/DC converter supplying optimal power, while the battery is used to supply or absorb power passively. The actual work is a continuity of several former research works. In fact, this system was previously studied using a 3-level converter as a DC/DC converter (Figure 1). The system has a two inverter/motor set. For simplicity, only one set will be shown. Part of the present study will complete the 3-level DC/DC analysis integrating the SVPWM modulation, dead-time delays control, and IGBT switching power losses computation.

The controls of the two converters in Figure 1 are independent. The use of a ZSI topology, instead, will have, as a first view, two advantages: only one single IGBT switch is used by the DC/DC converter and the control of this switch is directly linked to the modulation technique used by the Z-source inverter. The main contribution of the paper is to propose a simulated comparison between the two cited semi-active configurations (based on a 3-level converter, and based on a Z-source converter) integrated into a hybrid energy storage system supplying a two-front wheel driven electric vehicle. The nature of the information collected and implemented regarding the overall system will be exhaustively discussed in the present paper. The comparison is based on switches' voltage stresses, power efficiency of the whole conversion powertrain, and delivered braking torque spectral analysis.

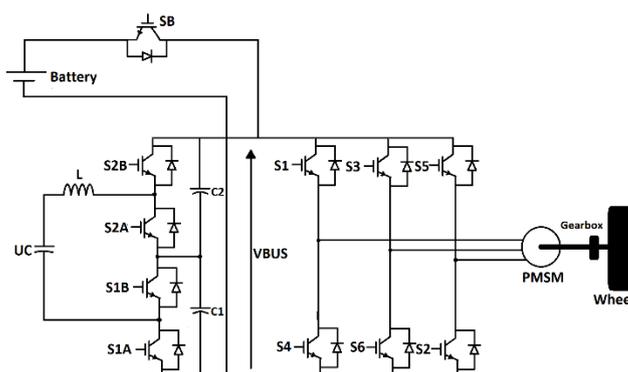


Figure 1. 3-Level DC/DC converter and SVPWM inverter in a semi-active topology.

The objective of the present paper is not to promote one solution over another. Indeed, in our problem, the state evolution of each system depends on intrinsic parameters, controlled and uncontrolled inputs as well as the modeling choices that were made. Furthermore, we pay particular attention to improving the behavior of the system by upgrading the control system to ameliorate the system response (leading to a more complex control design) and by adding passive filters to minimize harmonics and signal distortion. In fact, the control design always falls into a compromise between robustness regarding model uncertainties and noise inputs, real time implementation, and high computational performance requirements in terms of processor speed and memory capacity.

The comparison will be carried out on a previous 3-level conversion topology discussed by authors with some additions and the Z-source converter. The latter is known for its integrated DC/DC and DC/AC conversion control and especially for the advantage of use of a single IGBT for the DC/DC structure side.

The paper layout is as follows: The Z-source is introduced in Section 2. General configurations, control structures, modulation techniques, and their relative electrical and gain equations will also be defined. Two types of control strategies will be presented (an open-loop and a closed-loop control). The 3-level converter is discussed in Section 3. The selection of IGBTs used for power loss calculations and thermal behavior of the system is also discussed in Sections 2 and 3. Section 4 describes the thermal model and power loss computation adopted. After briefly describing the whole system model including the vehicle dynamic, the braking control, and the electric motors' control strategies, simulation tests are performed, and the comparison analysis is provided in Section 5.

2. Z-Source Converter

2.1. Introduction

As mentioned, the Z-source inverter is a type of power converter which combines the functions of the DC-DC converter and the voltage source inverter. It can boost the DC input voltage with no requirement of a DC-DC boost converter or a step-up transformer. It provides both voltage buck and boost properties, which cannot be achieved with traditional inverters. The comparison among conventional DC-DC-boosted PWM inverter and the Z-source inverter shows that the Z-source inverter needs the lowest semiconductors and circuit control.

In traditional voltage source inverters, there are eight switching states (6 active states and 2 zero states). While the Z-source inverter comprises an additional zero state called shoot through zero state where the upper and lower switches in one or more phase legs are gated on simultaneously. This state, forbidden in a voltage source inverter, makes the Z-source inverter more robust against electromagnetic interference and parasitic turn-on of devices.

2.2. Z-Source Structures Fundamentals

This subsection will be dedicated the summary of Z-source basics. The bidirectional quasi Z-source (BqZSI) will be chosen as the technological solution for the system.

2.2.1. Three-Phase Z-Source Inverter

The configuration of a conventional 3-phase Z-source inverter is shown in Figure 2. The DC-link circuit is a symmetrical X shape network consisting of two identical inductors (L_1 and L_2) and two identical capacitors (C_1 and C_2). The X shape forms a unique impedance network to avoid short-circuits when the device is in the shoot-through mode. Capacitor voltages and inductors' currents are the same.

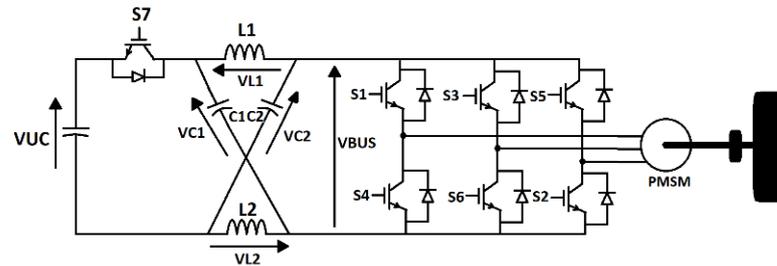


Figure 2. 3-phase Z-source inverter.

Figure 3 shows ZSI in shoot-through state and non-shoot-through state. During the ST state, the source diode is reverse biased and the input is disconnected from the rest of the circuit. The energy is transferred during this state from C_1 to L_1 and from C_2 to L_2 ($V_C = V_L$). The equation that governs this operation is

$$V_{BUS} = V_{C1} - V_{L1} = V_{C2} - V_{L2} = V_C - V_L = 0. \tag{1}$$

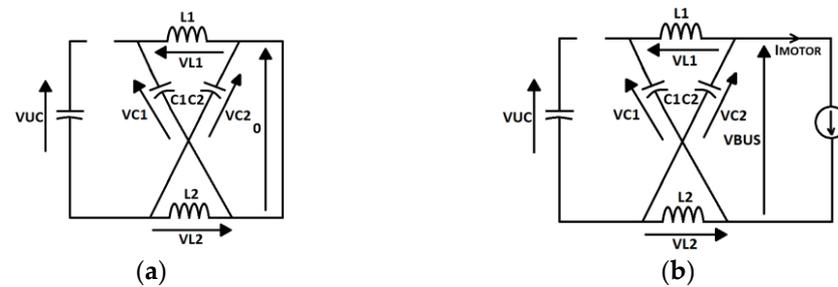


Figure 3. Equivalent circuit of a ZSI: (a) Shoot-through state; (b) non-shoot-through state.

During non-shoot-through, diode D is forward biased, and the inverter bridge acts as a current source. The energy is transferred from L_1 to C_2 and L_2 to C_1 . The equation that governs this operation is

$$V_{UC} = V_{C1} + V_{L2} = V_{C2} + V_{L1} = V_C + V_L. \tag{2}$$

The equation of V_{BUS} in the non-shoot-through state could be written as

$$V_{BUS} = 2V_C - V_{UC} = \frac{T}{T_1 - T_0} V_{UC} = B \times V_{UC}, \tag{3}$$

where T is the switching period, T_0 is the shoot-through period, and T_1 is the non-shoot through period. B is called the boost factor and can be expressed as

$$B = \frac{T}{T_1 - T_0} = \frac{1}{1 - 2D_0}, \tag{4}$$

D_0 is the shoot-through duty cycle ($D_0 = \frac{T_0}{T}$).

The average DC bus voltage applied to the inverter is

$$\langle v_{BUS}(t) \rangle = \frac{1 - D_0}{1 - 2D_0} V_{UC}. \tag{5}$$

In the traditional 3-phase PWM inverter, the equation of AC output voltage is

$$V_{OUT} = M \times \frac{V_{UC}}{2}, \tag{6}$$

M is the index of modulation.

In the Z-source inverter, boost factor B is added to the parameter control having

$$V_{OUT} = B \times M \times \frac{V_{UC}}{2} = G \times \frac{V_{UC}}{2}, \tag{7}$$

$G (= B \times M)$ is the inverter gain.

The advantage of this structure is that it realizes the boost function and DC-AC conversion in one active stage. This configuration of ZSI inverter has a unidirectional power flow, discontinuity of the input current, high inrush startup current, high voltage stress on switches, and high voltage across capacitor.

2.2.2. Bidirectional Quasi-Impedance Source Inverter

The quasi-impedance source inverter (qZSI), ref. [38] was proposed to overcome the problems of ZSI. The improved Z-source network has a continuous input current, a low voltage stress on capacitors, and sharing of the input/output grounds. As shown in Figure 4, to achieve power bidirectionality, the diode is replaced by active switch S_7 with a reverse diode. S_7 operates during the regenerative mode and its signal is the complement of the shoot-through signal.

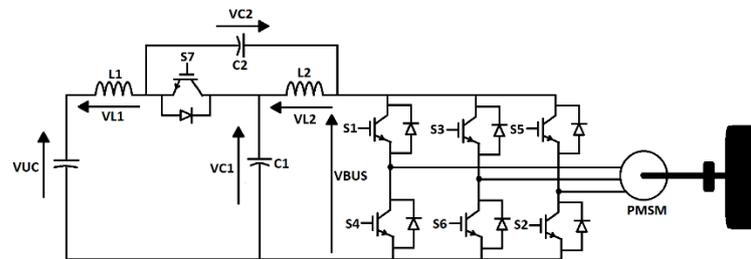


Figure 4. BqZSI structure.

The DC bus link ripple current will be absorbed by the two capacitors, which provides a better operating condition for the UC pack. The discontinuous conduction mode can be avoided and the BqZSI has better performance under low power factor condition (motor operating at light load). The BqZSI will be adopted in our study.

2.2.3. Passive Components Requirements

Passive components can be chosen depending on the capacitor voltage ripple and the inductor current ripple of the Z-source inverter as

$$C = \frac{P_{max} T_S D_0}{V_{UC} \Delta V_C}, \text{ and} \tag{8}$$

$$L = \frac{V_{UC} (1 - D_0) T_S D_0}{\Delta I_L (1 - 2D_0)}, \tag{9}$$

where P_{max} is the maximum output power, T_S the switching period, D_0 the shoot-through duty cycle, V_{UC} the voltage of the ultracapacitor (taken at its minimal value), ΔV_C the required capacitor voltage ripple, and ΔI_L the required inductor current ripple.

2.2.4. Types of Modulation Techniques

In Z-source inverter, there are various types of PWM techniques used for control. These techniques are simple boost control (SBC), maximum boost control (MBC), maximum boost control with third harmonics injection, maximum constant boost control (MCBC), and maximum constant boost control with third harmonic injection [39]. The MCBC with third harmonic injection will be selected as the most appropriate for our study.

The SBC is derived from the traditional sinusoidal PWM. Its main drawback is the restriction of the shoot-through time period (and thus the limitation of the boost factor) as well as an increasing device stress and switching power loss. Whereas the boost factor in the MBC technique will reduce the devices' stress in comparison to SBC. Nevertheless, the variation of shoot-through time period, generating low frequency harmonics in the passive component, will increase the cost and volume of the circuit.

MCBC (Figure 5) uses maximal boost factor and constant shoot-through time period to eliminate low frequency surge in the impedance-source network components. The MCBC will be also implemented using third harmonic injection by replacing the modulating signals by $1/6$ third harmonic injection to increase the modulation index by $\frac{2}{\sqrt{3}}$. It will be used in our study to conduct the comparison with the 3-level/SVPWM configuration.

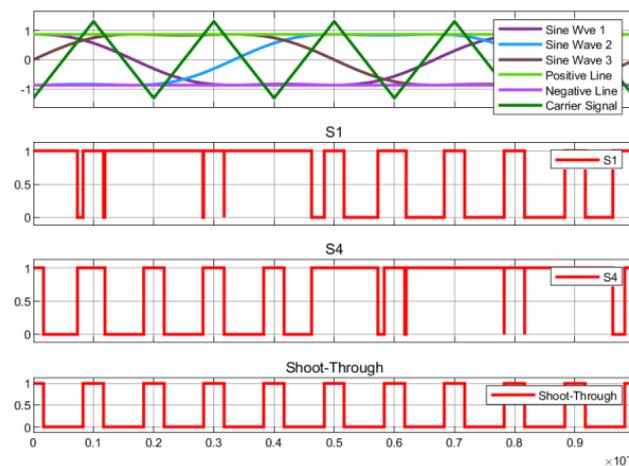


Figure 5. MCBC-3rd harmonic injection modulation technique.

2.2.5. Open-Loop Control

Based on Equation (5), the average value of the DC bus voltage can be determined according to the modulation technique. The open-loop control consists of imposing the index modulation M to the modulation strategy control system according to the required average DC bus voltage value.

We note that the voltage across the ultracapacitor is a fast-changing value depending on the rate of the input braking power. For the MCBC control technique, the equation is expressed by

$$M = \frac{2 \langle v_{BUS}(t) \rangle}{\sqrt{3}(2 \langle v_{BUS}(t) \rangle - V_{UC})}. \quad (10)$$

2.2.6. Closed-Loop Control DC Side Control

The modeling and control of a bidirectional ZSI can be found in the literature [40]. The authors in [41] worked on a small signal model of a BqZSI. In a Z-source, the average

voltage value applied to the DC bus is equal to the average voltage value of the capacitor C_1 (or C_2),

$$\langle v_{BUS}(t) \rangle = V_C. \tag{11}$$

The transfer function of the capacitor voltage, $v_{c1}(s)$, can be expressed as

$$v_{c1}(s) = G_{v2d}(s) d(s) + G_{v2in}(s) v_{UC}(s), \tag{12}$$

The ultracapacitor voltage is considered as a disturbance effect in the closed loop control structure. A PI antiwindup controller will be used for the DC bus voltage controller in order to compensate the saturation effect (Figure 6).

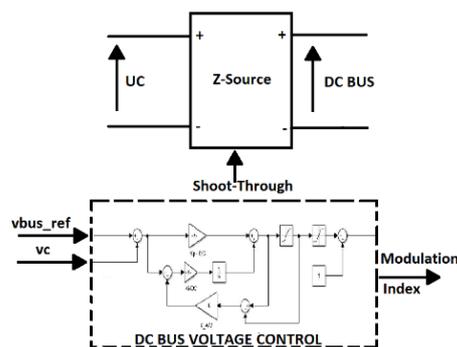


Figure 6. DC bus control structure.

AC Side Control

From the AC side control (Figure 7), the objective is to provide a braking torque at a certain wheel speed rotation. The authors have already described the torque control strategy based on the regeneration of current references in [42].

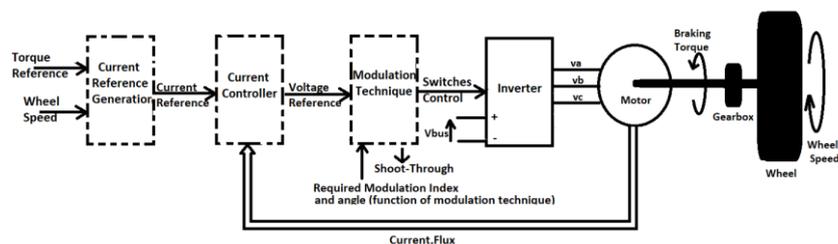


Figure 7. AC control structure.

2.2.7. IGBT Power Switches Selection for Z-Source Closed-Loop Control

For designing the electric power system, three major issues come to mind, such as losses, harmonics, and electromagnetic interference. The mentioned issues affect the system in terms of quality, efficiency, size, and cost. The key factor in a power converter is power switches; categorized based on capability of handling the power and their switching speed maximizing voltage blocking and carrying current.

An IGBT cannot withstand a surge current pulse due to the current saturation characteristic of the device. In failure conditions, the IGBT switches are driven into pulse-blocking mode. This leads to an asymmetric short circuit of the load producing additional stress due to the undesirable alternating torques.

Robustness of IGBTs regarding surge-current mode is then essential for some failure modes. A comparison of different IGBT designs at 4 ms and 10 ms high-current pulses with an applied gate voltage above the rated datasheet value is performed in [43]. The authors showed that the increase of the emitter side carrier concentration leads to lower power dissipation and thus to an increased robustness during surge current events. the

authors in [44] studied the possibility of symmetrizing the load short circuit with active turned on IGBTs.

For reasons that will be explained later, the selection of the IGBT for the BqZSI will be based on the closed-loop control. Due to the high loss in the ST switching device, a proposition will be made to replace the switching device with two parallel switching devices with lower current specifications in order to minimize the losses and thus the temperature rise within the semi-conductor module (Figure 8).

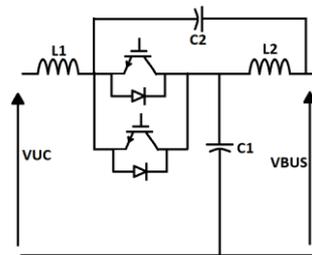


Figure 8. Two shoot-through switching devices in parallel.

We have chosen Fuji Electric as the manufacturer of the IGBT modules. Our main criteria are to maintain the same manufacturer for all the power switches used in our two compared power structures. The selection of the power semi-conductors is based on the current/voltage ratings obtained during simulation. The choice of power switches is made based on matching between voltage and current ratings during the critical transient phenomenon [45].

For the ST IGBT device, 1MBI900V-120-50 (1200 V/900 A) will be used for the parallel switching device structure, and 1MBI600VC-120 P (1200 V/1600 A) for the single switching device structure. For the Z-source inverter, 6MBI300V-120-50 (1200 V/300 A) will be selected.

3. Level DC/DC Converter Associated to a SVPWM Inverter

3.1. Introduction

A three-level DC/DC converter is proposed as the interface between the UC and the DC bus (Figure 1). This power architecture allows the UC voltage to be used in a large range, maximizing the energy recovered. The reasons for choosing a three-level converter are justified in minimizing by half, the voltage constraints across the electronic power switches, reducing the size of the inductor and improving the dynamic performance of the converter. However, the controller design is much more complex relative to a classical buck-boost converter topology which involves UC and batteries HESS [46–48]. The authors have already proposed the proper sizing of elements and control design of the 3-level converter in [49]. Dead-time delay and space vector pulse width modulation is added to the system control simulation.

3.2. 3-Level DC/DC Converter Control Introduction

Three control loops were designed in [49]. The variables to be controlled are the UC current and voltage as well as the voltage of the DC bus. From the UC current point of view, the DC/DC converter is modeled in Figure 9 as a large-signal average model.

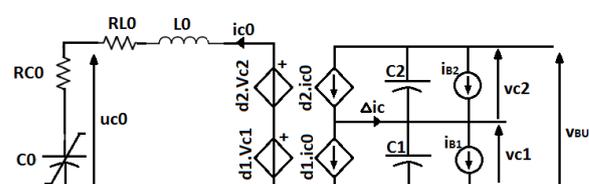


Figure 9. Large-signal average model of 3-level DC/DC converter [47].

The capacitance C is expressed by:

$$C \geq \max \left(\frac{P_{max} (1 - 2D_{min})}{\Delta V_{BUS} V_{BUS} f_{sw}}, \frac{P_{max} (3 - 2\sqrt{3})}{\Delta V_{BUS} V_{BUS} f_{sw}} \right), \tag{13}$$

where f_{sw} is the switching frequency, V_{BUS} the DC bus voltage, ΔV_{BUS} the required DC bus voltage ripple, D_{min} the minimal duty cycle, and P_{max} the maximal admissible power.

The inductance L_0 is expressed by

$$L_0 \geq \frac{V_{BUS,max}}{16 \Delta i_{c0max} f_{sw}}, \tag{14}$$

where $V_{BUS,max}$ is the maximal DC bus voltage and Δi_{c0max} the maximal ultracapacitor current ripple.

The duty cycles d_1 and d_2 are generated by nonlinear controller such that

$$d_1 = \frac{u_0 + u_{c0}}{v_{BUS}} + \Delta d, \text{ and} \tag{15}$$

$$d_2 = \frac{u_0 + u_{c0}}{v_{BUS}} - \Delta d, \tag{16}$$

where u_0 is the main control variable generated by the current controller, and $\Delta d(s)$ is the balancing duty cycle as an auxiliary control variable generated by the voltage error controller ($\Delta v_c(s)$).

The transfer functions in a matrix form can be expressed as

$$\begin{bmatrix} i_{c0}(s) \\ \Delta v_c(s) \end{bmatrix} = \begin{bmatrix} G_{ic0u0}(s) & G_{ic0\Delta d}(s) \\ G_{vcu0}(s) & G_{vc\Delta d}(s) \end{bmatrix} \begin{bmatrix} u_0(s) \\ \Delta d(s) \end{bmatrix} + \begin{bmatrix} G_{ic0B}(s) \\ G_{vcB}(s) \end{bmatrix} \Delta i_B(s), \tag{17}$$

where $[i_{c0}(s), \Delta v_c(s)]^t$ is the output vector, $[u_0(s), \Delta d(s)]^t$ is the control vector, and $\Delta i_B(s)$ is the perturbation signal.

Whereas for the control of the voltages, the transfer functions of the system can be deduced as

$$\begin{bmatrix} u_{c0}(s) \\ v_{BUS}(s) \end{bmatrix} = \begin{bmatrix} G_{c0}(s) \\ G_{BUS}(s) \end{bmatrix} i_{c0}(s) + \begin{bmatrix} 0 \\ G_P(s) \end{bmatrix} p_{LOAD}(s). \tag{18}$$

The transfer function $G_{c0}(s)$ relates the UC voltage $u_{c0}(s)$ to the UC current $i_{c0}(s)$. The DC bus voltage $v_{BUS}(s)$ is related to $i_{c0}(s)$ taken as a control variable (according to the $G_{BUS}(s)$ transfer function) and to the load power p_{LOAD} taken as a perturbation variable (according to the $G_P(s)$ transfer function). The right-half plane zero present in the transfer function G_{BUS} had been neglected due to the fact that the switching frequency is quite greater than the bandwidth of the DC bus voltage control loop in the DC/DC converter.

The DC bus voltage control scheme is presented in Figure 10.

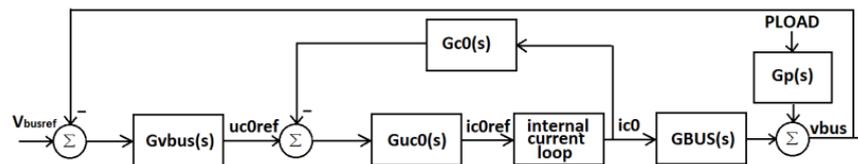


Figure 10. DC bus voltage controller.

3.3. DC/AC Converter Modulation Technique

The generation of reference voltages for the SVPWM control are based on an optimal traction and regenerative braking current synthesis for an IPMSM using three combined torque control methods for an EV. In order to impose torque at higher speed, the flux should

be weakened by injecting negative current into the d -axis. The torque/speed characteristics will be decomposed into five zones of operation delimited by three methods of control: maximum torque per ampere, voltage current limited maximum torque, and constant power region [42].

SVPWM allows higher output voltage compared to the conventional sinusoidal PWM technique. Its implementation is based on the search of the fundamental voltage reference in the $\alpha\beta$ plan and on finding the proper applied voltages by the inverter to be imposed and their respective times in one commutation period. The symmetrical distribution of the voltages in a onetime commutation period assures low harmonics distortion.

3.4. IGBT Power Switches Selection

Based on Fuji Electric IGBT modules, electrical and thermal specifications are determined. The 2MBI300VB-060-50 (600 V/300 A) module was selected for the 3-level DC/DC converter, and the 6MBI180VB-120-50 (1200 V/180 A) module for the inverter.

4. Thermal Model and Power Loss Calculation

4.1. Introduction

In terms of power losses in the electric vehicle traction system, the thermal behavior of each major element should be analyzed (storage elements, static and dynamic power converters, passive elements, etc.). The losses in the storage elements involve a deeper study of the physical effects and electrochemical effects. Among the drive losses, control circuit losses and passive elements losses, the semiconductor power switching devices' losses are the largest. In this paper, the focus is concerned with the static power converters' losses. Calculating power losses of the inverter plays an important role in improving system efficiency and power density, designing heat dissipation system, and selecting power devices.

The DC/DC converter and inverter losses include the IGBTs and diode losses in terms of conduction and switching losses. Estimation of IGBT switching losses under hard switching using Matlab/Simulink for a step-down converter is proposed in [50]. The authors in [51] used a parabola interpolation method to calculate the inverter losses. The method used parabola interpolation to fit the losses' characteristic curves instead of linear approximation. High switching frequency is always desired for better performance in terms of signal distortion. In [52], the frequency dependent switching power losses are discussed for a 5 MVA multi-level converter. A synthesis of different electrical methods used to estimate the temperature of power switching devices with thermo-sensitive electrical parameters is discussed in [53]. These methods are then compared in terms of sensitivity, linearity, accuracy, genericity, calibration needs, and possibility of characterizing the thermal impedance or the temperature during the operation of the converter. A general method for estimating junction temperature is studied by using an adaptive thermal network model in [54], considering both the temperature influence on the extraction of parameters and the errors caused by the physical structure.

In this paper, look-up tables based on manufacturers' data sheet specifications for IGBTs would be used in order to compute power switch losses.

4.2. Thermal Model

Thermal state-space representations for IGBT and its reversed diode are used to build a Cauer network. The state space equations are given below

$$\dot{X}(t) = \left[-\frac{1}{R_{th} C_{th}} \right] X(t) + \left[\frac{1}{R_{th}} \quad \frac{1}{C_{th}} \right] \begin{bmatrix} T_c \\ P_l \end{bmatrix}, \text{ and} \quad (19)$$

$$\begin{bmatrix} T_j \\ P_c \end{bmatrix} = \begin{bmatrix} 1 \\ \frac{1}{R_{th}} \end{bmatrix} X(t) + \begin{bmatrix} 0 & 0 \\ -\frac{1}{R_{th}} & 0 \end{bmatrix} \begin{bmatrix} T_c \\ P_l \end{bmatrix}, \quad (20)$$

where, T_j is the junction temperature of the device (IGBT or reversed diode), P_c the heat flow from junction to case, P_l the power loss across the device, T_c the case temperature, R_{th} the junction to case thermal resistance, and C_{th} the thermal capacitance of the junction. The junction temperature T_j will be used as input in the look-up tables to calculate the power losses in the device, and P_c as input to Simscape models to compute case and sink temperatures.

4.3. Power Losses Calculation of the Switching Devices

Power switch device computations will be based exclusively on manufacturer data sheet specifications and characteristics curves for IGBT and its reversed diode.

4.3.1. IGBT Power Losses

For IGBT, turn-on and turn-off switching losses (E_{on} and E_{off}) are a function of supply voltage (V_{CC}), collector current (I_C), and junction temperature (T_j). Collector-emitter voltage V_{CE} (on-state), used to quantify conduction power losses, depends on I_C and T_j . Three look-up tables (two 3-dimensional tables and one 2-dimensional table) are used to generate energy switching losses and V_{CE} (Figure 11).

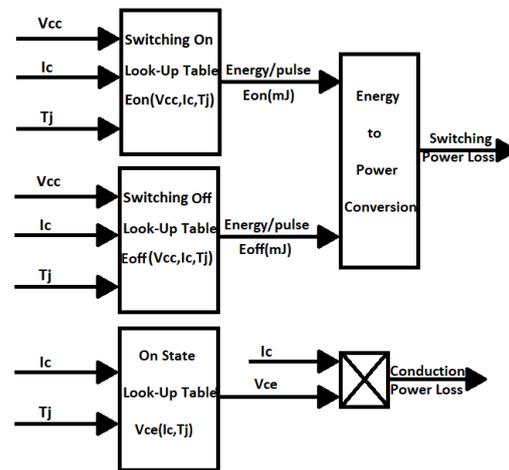


Figure 11. IGBT power losses calculation using look-up tables.

4.3.2. Diode Power Losses

For the diode, reverse recovery energy loss (E_{rec}) is a function of the supply voltage (V_{CC}), the forward current (I_F), and the junction temperature (T_j). Forward voltage V_F (on-state), used to quantify conduction power losses, depends on I_F and T_j . Two look-up tables (one 3-dimensional table and one 2-dimensional table) are used to generate energy recovery losses and V_F (Figure 12).

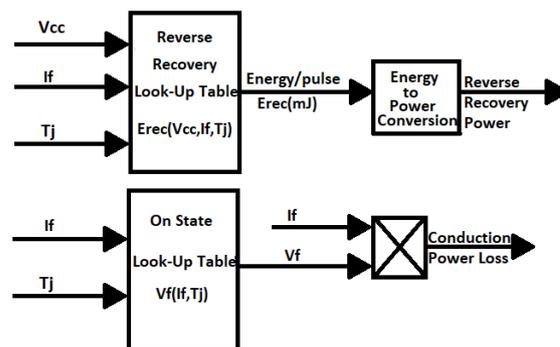


Figure 12. Reversed diode power losses calculation using look-up tables.

5. Simulation Validation

5.1. Introduction

HESs are generally implemented to minimize the stress on the battery (as a main energy density storage element) and to respond to the required dynamic power demand during high current demand. In our scope, focusing on the battery lifespan, a non-classical but extreme regenerative braking will be taken into account for the generation of the high-power demand. The test is performed under asphalt dry road conditions. The driver requires an urgent and immediate full stop. This maneuver will be investigated in the present paper.

Previous works from authors have validated some of the Simulink block models. The developed subsystems (vehicle model, driven motors model, 3-level-based converter, and associated controllers) are modeled starting from their primitive state equations. This design strategy will still be adopted for the actual work facilitating integration of new blocks in the global system. This choice has been adopted in order to analyze and study multiple fields such as the effect of the braking action on the vehicle behavior and on the energy regeneration [55] as well as the control of the 3-level converter based in the HES [49]. In the mentioned papers, the inverter was taken as a unity gain in order to accelerate simulation time. Even so, the performed simulation tests were highly time and memory consuming.

The simulation tests will be used to compare two semi-active HES structures. The HES has a Li-ion battery as the main energy storage system and an ultracapacitor as a secondary energy storage. The first HES is quasi-bidirectional Z-source based, while the other is 3-level DC/DC-inverter based. The UC is sized to absorb the total braking energy. The simulation tests should show the response of each system according to its control design, the power efficiency of the overall power conversion chain, the signals distortion in terms of UC voltage and braking torque, the voltage stress on the semiconductor power switching devices, and the thermal behavior of the system in terms of temperature increasing.

The simulations will be performed on a high-friction good-condition asphalt road. The objective is to impose a maximal power flow, recovered from the wheels, and passing throughout the motors, generating a peak power of 60 kW, inverters, DC/DC converters, and to be stored in the ultracapacitor. This power is conveyed according to the numerous control systems implemented throughout the various converters in order to reach the secondary energy storage system. The choice of an initial speed at 80 km/h was made for two main reasons. The first reason is that the ECE R13H regulations [56] recommend a 'type 0 braking test' at speed of 80 km/h for M1 category vehicles (same as the used electric vehicle). The braking control system was developed and validated by the authors [55]. The second reason is that the maximum power of 60 kW is generated from a speed relatively close to 50 km/h performed on a high-friction good-condition asphalt road. The vehicle will initially be running at 80 km/h in order to reach more than 60 kW as maximal power input (depending on the 30 kW IPMSM motor's control [42]). The test will impose high voltage stress on the switching devices and high-power losses.

5.2. Simulation Models

The model of the vehicle is developed using the basic state primitive equations to analyze the kinematics and dynamics of the vehicle with respect to the braking and traction methods validated [55]. The Simulink model of the conversion powertrain contains all the storage elements, the models of the two IPMSM motors, the converters, the associated control systems, the thermal behavior models, etc.

Figure 13 shows the general blocks of the overall system models. The inputs are:

- the initial speed of the motor (at rest for traction test or running at a certain velocity for braking test);

- the steering angle (taken at 0°) of the front wheels: the test is performed for a longitudinal motion. The vehicle model has been developed using the dynamic and kinematic equations of a 5 DoF (degrees of freedom) vehicle (four rotation wheels and yawing movement). The actual objective is to evaluate the HESS performance in terms of electrical transients' comportment, energy recuperation, and power efficiency. The stability of the vehicle in extreme braking condition while steering is yet an interesting research topic;
- The braking or acceleration severity: extreme braking conditions will be performed assuring extreme power regeneration depending on the road condition and wheels' torque distribution according to the ECE R13H assuring safety of the driver on one hand and the stability of the vehicle on the other hand;
- Road conditions: which will have a significant effect on the torque distribution and thus on the power regeneration scope at the HESS level.

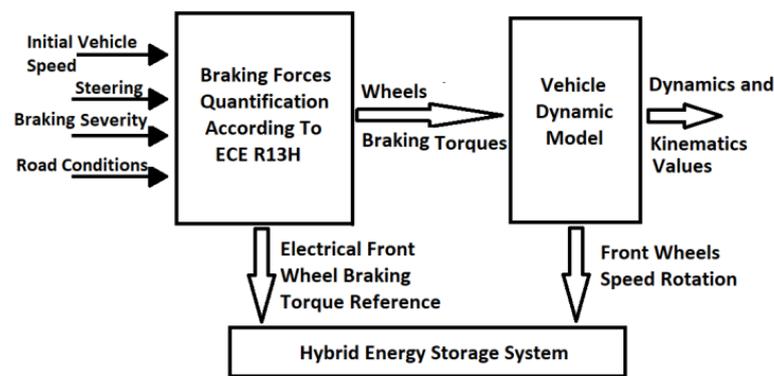


Figure 13. General overall system.

Figure 14 shows overall models involved in the HESS block. The battery interference will not be treated in the current paper. The UC handles all the power regenerated by the braking which will minimize the battery solicitation during extreme maneuvers.

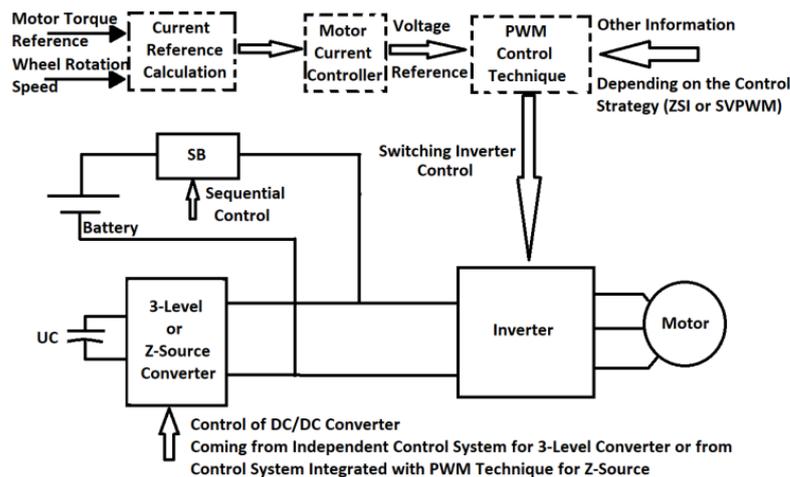


Figure 14. Models at HESS level.

5.3. Simulation Tests

5.3.1. Simulation Set-Up

Even though the modeling complexity of the overall system is obvious, the obtained results, at each step, demonstrate the realistic and the coherent aspects of the designed models and controls, showing more and more the functionality of the simulation as a first step to the final prototype.

The software simulation tool used is Matlab/Simulink. At the start of the simulation, a Matlab script will be launched to execute multiple sub-programs in order to define the mandatory elements of the system: (1) the vehicle parameters, the external dynamic, and the road type parameters; (2) the vehicle driving motor's parameters, limiting operation points, and regulators gains; (3) the HESS characteristic parameters, sizing elements, and corresponding regulators' gains of the different power electronics-based converters; and (4) the semi-conductors' power switches device look-up tables generation and data specifications definition.

For each test, after defining the initial system state (initial vehicle velocity, initial UC voltage, road surface type, etc.) and generating all the above quantities, a Simulink program will be launched. At the end of the simulation, a Matlab program script plots the corresponding figures and delivers the characteristic values of the overall system.

5.3.2. Simulation Parameters

A list of parameters is summarized in the following tables (Tables 1–4).

Table 1. IPMSM used parameters [42].

Parameter	Symbol	Value
Stator winding resistance	R_s	0.45 (Ω)
d -axis inductance	L_{sd}	0.54 (mH)
q -axis inductance	L_{sq}	1.05 (mH)
Number of pole pairs	p	3
Permanent magnet flux linkage	Λ_m	0.148 (mWb)
Viscous friction coefficient	B	0
Motor moment of inertia	J_m	0.3 ($\text{kg}\cdot\text{m}^2$)
Nominal power	P	30 (kW)
Line to line voltage	V_{sn}	230 (V)
Maximal current	I_{sn}	100 (A)

Table 2. UC characteristics.

Parameter	Symbol	Value
Maximal voltage	U_{c0max}	325 (V)
Minimal voltage	U_{c0min}	165 (V)
Stored energy	E_s	108.8 Wh (SoC _{min} = 25%)

Table 3. 3-Level DC/DC/SVPWM topology.

Parameter	Symbol	Value
Ultracapacitor Capacitance	C_0	10 (F)
Ultracapacitor Series Resistance	R_{C0}	70 (m Ω)
Inductor	L_0	50 (mH)
Inductor Series Resistance	R_{L0}	50 (m Ω)
Capacitors	C	1 (mF)
Capacitors Series Resistance	R_{L0}	20 (m Ω)

Table 4. Bidirectional quasi-Z-source inverter topology.

Parameter	Symbol	Value
Inductors	L_1, L_2	0.1 (mH)
Inductor Series Resistance	R_{L1}, R_{L2}	20 (m Ω)
Capacitors	C_1, C_2	706 (μF)
Capacitors Series Resistance	R_{C1}, R_{C1}	18 (m Ω)

The voltage of the ultracapacitor has a minimal voltage of 165 V and a maximal voltage of 325 V. The tests have been performed at minimal voltage in order to size power switches at their highest current values.

The nominal voltage of the Li-ion battery is taken as 360 V. The regulated DC bus voltage is set to 400 V.

5.3.3. Controller Settings

The gain controllers for the two topologies are expressed in Table 5. The vehicle model sizing is based on a Nissan Leaf electric vehicle. Two identical 30 kW motors will be used. We note that the BqZSI modulation technique selected to do the comparison with the 3-level/SVPWM topology is the MCBC control with third harmonic injection. The selection is due to the advantages with regards to the other techniques enumerated previously. The switching frequency is chosen to be 5 kHz.

Table 5. Controllers settings and modulation techniques.

Topology	Controller Type	Gain	
Common Settings	Motor Current Controllers	<i>d</i> -axis Controller current	Proportional Gain k_{p_d} 0.54 Integral Gain k_{i_d} 450
		<i>q</i> -axis Current Controller	Proportional Gain k_{p_q} 1.05 Integral Gain k_{i_q} 450
		DC Bus Voltage Controller	Proportional Gain $k_{p_{vbus}}$ -0.1407 Integral Gain $k_{i_{vbus}}$ -13.2236 Antiwindup gain $k_{pw_{vbus}}$ -3
			UC Voltage Controller
UC Current Controller	Proportional Gain $k_{p_{ic}}$ 312.5 Integral Gain $k_{i_{ic}}$ 300		
	Modulation Technique	Space Vector PWM	
Bidirectional Quasi-Z-Source Inverter Topology	Index Modulation Controller	Proportional Gain k_{p_M} 0.0001 Integral Gain k_{i_M} 0.05 Antiwindup gain k_{pw_M} 1	
		Modulation Technique	Maximum Constant Boost Control with Third Harmonic Injection

5.4. Simulation Results and Comparison

The test simulation consists of the extreme braking maneuver of an electric vehicle initially running at 80 km/h. The type of road adopted is a good condition asphalt road. This typology of braking operation involves a worst-case scenario power flow according to the power converters and energy storage elements because it generates the highest electrical transients leading to extreme power losses and thus critical thermal constraints. Particular interest will be focused on voltage and current transients, overall powertrain efficiency, and thermal behavior, nevertheless one should not forget that the goal is to ensure a proper control of the braking torque and the voltage of the DC bus at 400 V.

5.4.1. Voltage and Current Stresses of DC/DC Converters IGBT Modules

The voltage stresses applied to one of the paralleled IGBTs of the Z-source and one of the IGBT of the 3-level DC/DC converter are showed in Figure 15. The peak voltage in the first case reaches 753 V and 287 V in the second case.

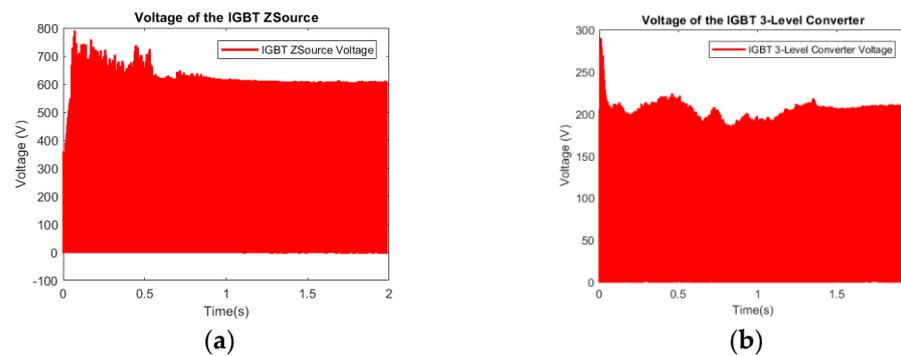


Figure 15. Voltage stresses applied in (a) the Z-source ST module and (b) one IGBT switch of the 3-level DC/DC converter.

The current flowing through each of the paralleled IGBT modules forming the Z-source converter has a transient of -745 A/ $+988$ A for the OL control, whereas it reaches a maximum current of -185 A/ $+658$ A for the CL control (Figure 16). The current flowing through one IGBT module in the 3-level DC/DC converter has a smoothed form reaching only 262 A. As a consequence, OL control will be excluded from the comparison race. This is due to the high transient values. In fact, the OL control is characterized by a sudden non-corrective response. The zero-order hold should be set at a sampling time of 10^{-7} which leads to high memory burden and excessive switching losses due to the impracticable switching frequencies. Choosing greater sampling time greatly deteriorates the controlled quantities (DC bus voltage and output torque).

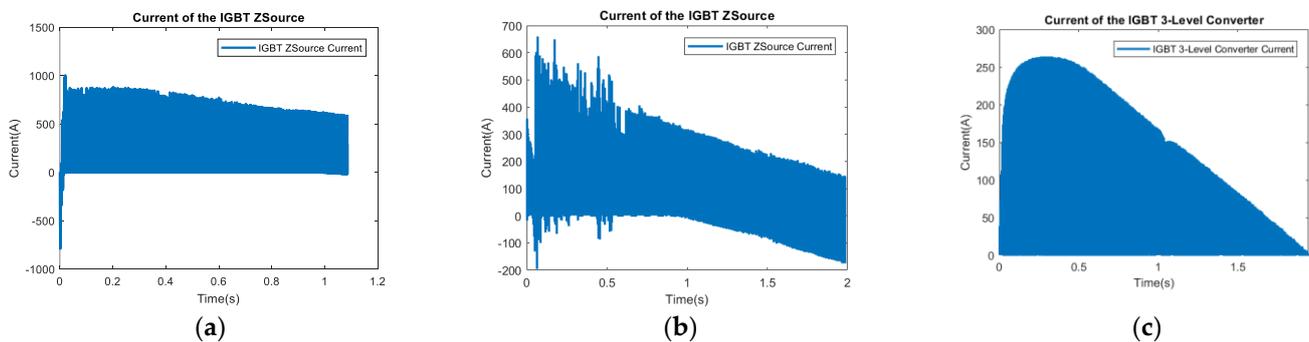


Figure 16. Current flowing in the ST-IGBT of the Z-source in an (a) open-loop control, (b) closed-loop control, and (c) the IGBT modules in a 3-level/SVPWM topology.

As mentioned before, the IGBT modules selected for the Z-source paralleled ST switch are the 1MBI900V-120-50 (1200 V/900 A) and 2MBI300VB-060-50 (600 V/300 A) for the 3-level DC/DC converter. In the case that one single switch should play the role of the shoot-through control, the 1MBI600VC-120 P (1200 V/1600 A) will be selected.

5.4.2. DC Bus Voltage

Figure 17 shows the DC voltage regulation for the BqZSI (open-loop and closed-loop control) and for the 3-level/SVPWM topologies. A low cut-off speed control has been integrated, in order to avoid a UC discharging current at low speed. At open-loop control (OL), the DC voltage reaches a peak of 1071 V. Whereas the peak voltage in closed-loop control reaches 753 V for the Z-source and 740 V for the 3-level/SVPWM topology.

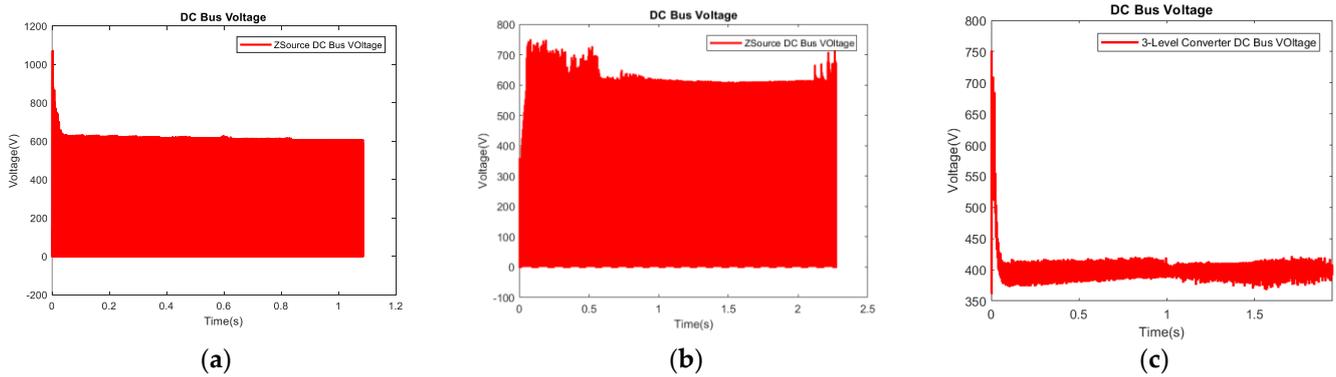


Figure 17. Voltage of the DC bus for (a) open-loop control BqZSI, (b) closed-loop control BqZSI, and (c) 3-level/SVPWM topology.

5.4.3. Voltage and Current Stresses of the Inverters IGBT Modules

The comparison will be focused between the closed-loop MCBC 3rd harmonic injection BqZSI and the 3-level DC-DC converter/SVPWM inverter. Figure 18 shows the current and voltage stresses of each of the inverters. As cited in Sections 2.2.7 and 3.4, the IGBT modules selected for the Z-source inverter are the 6MBI300V-120-50 (1200 V/300 A) and 6MBI180VB-120-50 (1200 V/180 A) for the inverter.

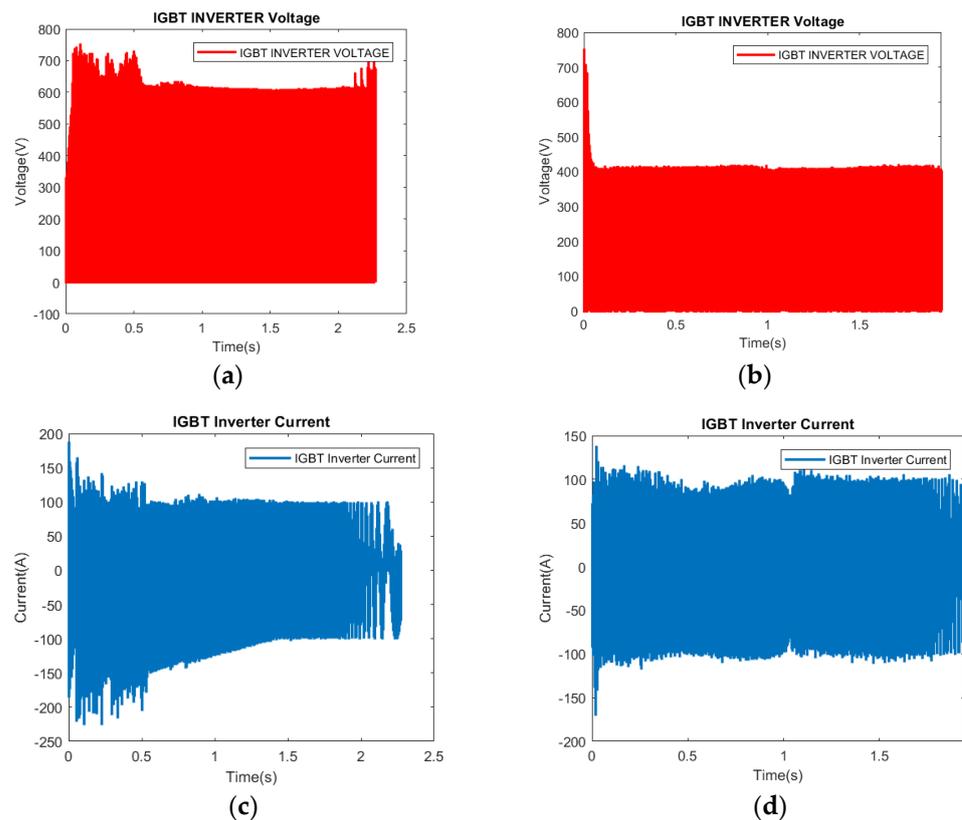


Figure 18. Voltage stress applied to inverter IGBT modules for the (a) MCBC 3rdH modulated inverter, (b) SVPWM inverter, as well as the current flowing through, respectively (c,d).

5.4.4. Ultracapacitor Voltage and Current

Figure 19 shows the voltage and charging current of the UC during the extreme braking maneuver. The UC voltage is initially set at its minimal value of 165 V. The voltage and current controls for the 3-level topology are obvious; whereas the charging current for the BqZSI topology has high discharging transient of -113 A and charging transient of

600 A with a steady current ripple of 130 A. This has a repercussion of a 9 V ripple voltage corresponding to the multiplication of the internal resistor of the UC ($R_{c0} = 70 \text{ m}\Omega$) and the current ripple. The current ripple could have significant effects on UC kinetic ageing and reliability. The bending curve of the UC voltage throughout the braking process is related to the decreasing current. According to the following equation:

$$V_{UC} = E + R_{c0} I_{UC}, \quad (21)$$

where E is the voltage of the UC at open circuit configuration, as the current decreases, the voltage drop across R_{c0} will also decrease.

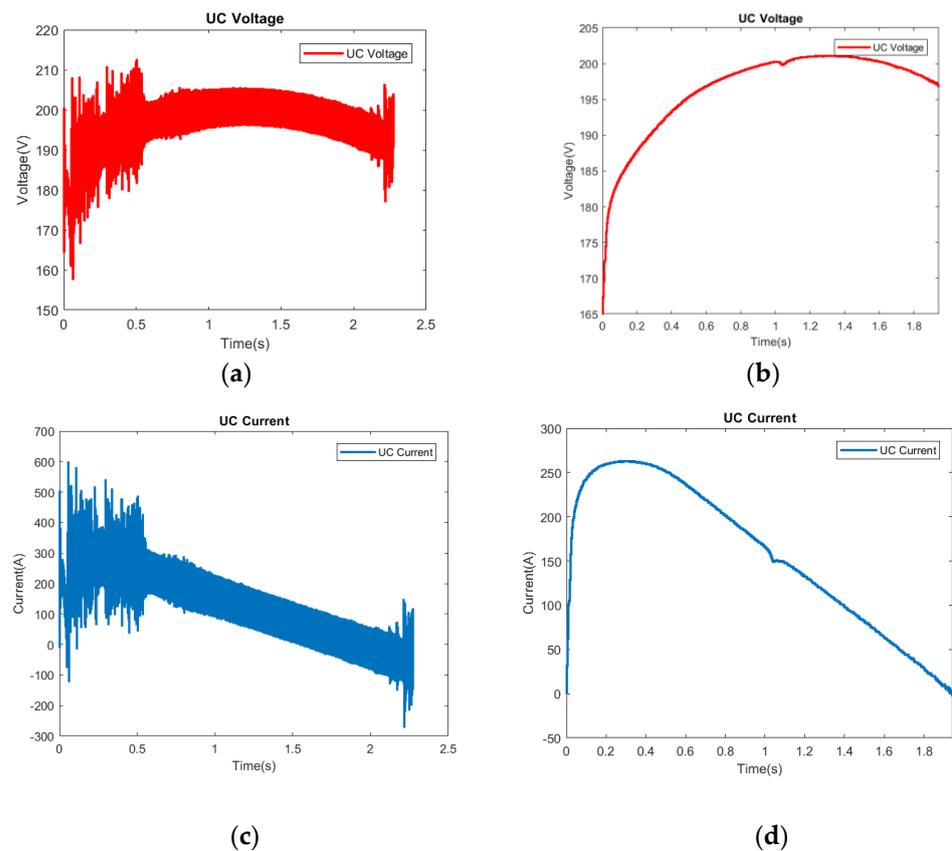


Figure 19. Voltage and charging current of the UC for the Z-source topology (a,c) and for the 3-level/SVPWM topology (b,d).

The voltage and current of the ultracapacitor are better controlled in the 3-level DC/DC converter HESS due to a stricter control than those in the bidirectional quasi-Z source inverter. The current in the latter contains a lot of ripples. Nevertheless, the mean values of the current and voltage of the UC are not very different than those ensured by the 3-level DC/DC converter. Indeed, the voltage of the UC at the end of the braking process is 196.9 V for the 3-level DC/DC converter and 194.2 V for the other structure. This slight difference of 2 V is due to a higher loss in the quasi-Z source inverter structure.

5.4.5. Torque and Power Generated

Figure 20 shows the total generated by the two 30 kW IPMSM motors and the associated braking torque for each motor for the two topologies. At the beginning of the braking, the torque ripples related to the BqZSI topology are higher than those of the SVPWM topology. At the end of the braking, the ABS system is activated ensuring a zero-torque reference. The torque control has responded accordingly. This phenomenon has not been realized on the SVPWM control due to the low cut-off speed control activation. According

to the optimal control methods designed by the authors [42], for a speed higher than the base speed (4457 rpm), the voltage and current limited maximum torque (VCLMT) could ensure more torque than the maximum torque per ampere control, and thus more power. The figure shows a power increase which could attain 69.5 kW for the BqZSI and 65.1 kW for the SVPWM inverter instead of a maximal power of 60 kW (for the two motors). The 4-kW difference between the two topologies ensures the boosting voltage capability of the Z-source converter.

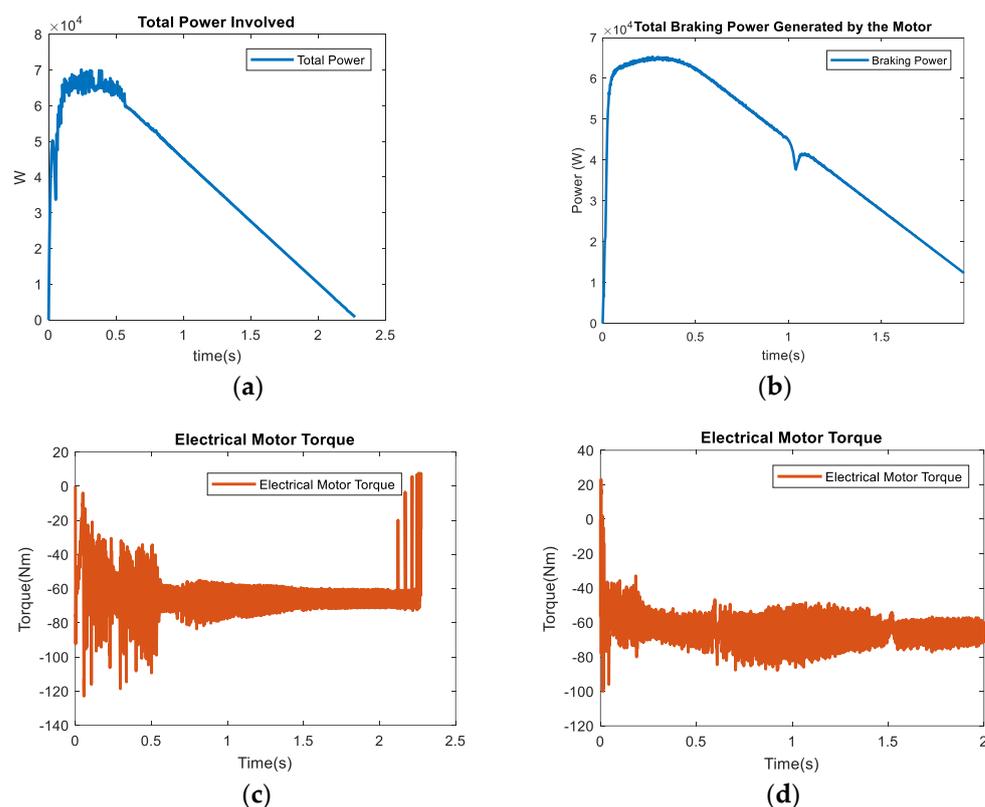


Figure 20. Total generated power by the two 30 kW motors and the associated braking torque for each motor for the BqZSI topology (a,c) and 3-level/SVPWM topology (b,d).

5.4.6. Power Losses and Efficiencies

Figure 21 shows the losses in the two topologies. According to Section 4.3, the losses cover commutation and conduction losses for the switching modules (IGBTs and reversed diodes). Results will focus on the overall losses for the DC/DC converters and inverters. The total power dissipated in the two paralleled ST switches is far less than the power dissipated in one single ST switch. At the peak power generation of 70 kW, the power loss attains 1100 W in lieu of 2275 W. The value of 1100 W is close to the 3-level DC/DC converter power losses which attain a value of 950 W at maximum power generation (of 65 kW generated in that case).

For the inverter losses, the advantage goes to the SVPWM inverter with losses remaining around 450 W all along the braking process. Whereas for the MCBC 3rdH inverter, power losses are increasingly higher than 1000 W, attaining 2500 W at lower speed.

In terms of efficiency (Figure 22), the 3-level/SVPWM holds a good efficiency throughout the braking process. The decreasing part corresponds to the constancy of the inverter loss even if the flowing power decreases. For the BqZSI topology, the efficiency is lower than that of the 3-level/SVPWM topology, especially at low speed due to the increasing power losses in the inverter.

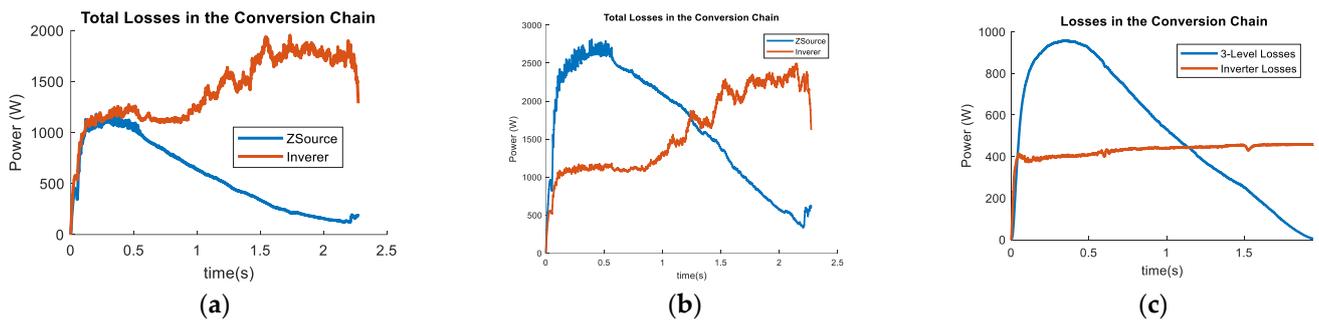


Figure 21. Overall losses in the powertrain conversion of (a) the two paralleled ST IGBT BqZSI topology, (b) single ST IGBT BqZSI topology, and (c) 3-level DC-DC/SVPWM.

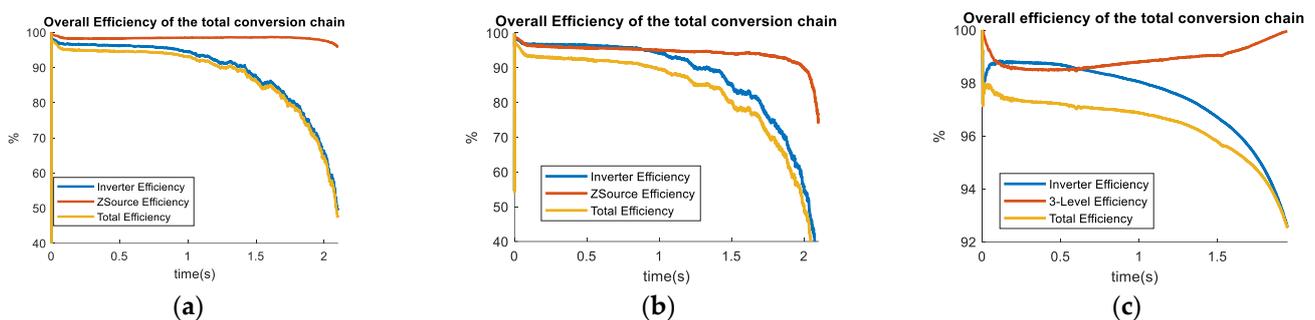


Figure 22. Overall efficiency of the powertrain conversion of (a) the two paralleled ST IGBT BqZSI topology, (b) single ST IGBT BqZSI topology, and (c) 3-level DC-DC/SVPWM.

The voltage and the current in the 3-level converter are highly controlled values (a). Its efficiency curve will follow, inversely, the power curve in Figure 21c (also depending on the controlled current, Figure 19d). At the end, as the transferred power tends to zero, the efficiency tends to 100%.

The speed trajectory of the vehicle is shown in Figure 23. The braking conditions are performed on a high friction coefficient road surface, according to the ECE R13 H regulation, with an anti-lock braking system (ABS) implemented in the braking control system.

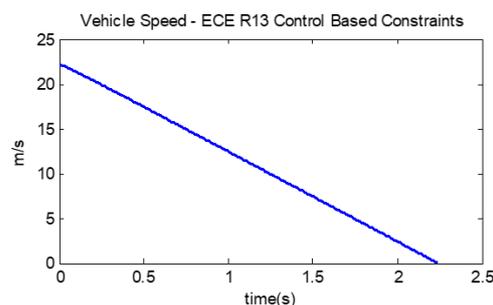


Figure 23. Speed trajectory (in m/s) for high friction coefficient road surface.

5.4.7. Temperature Evolution

The temperatures of the DC/DC converters are presented in Figure 24. It is good to mention that the heatsink selected has a sink to air ambient thermal resistance of $R_{th(s-a)} = 0.097 \text{ K/W}$. The thermal behavior of the two paralleled ST switches' DC/DC Z-source converter and of the 3-level DC/DC are quite acceptable. It is known that for the 3-level DC/DC converter, two modules of two IGBTs in one package has been used. Whereas, for the single ST IGBT package, the temperature of the module could exceed the limits and lead to an improper functioning of the IGBT module.

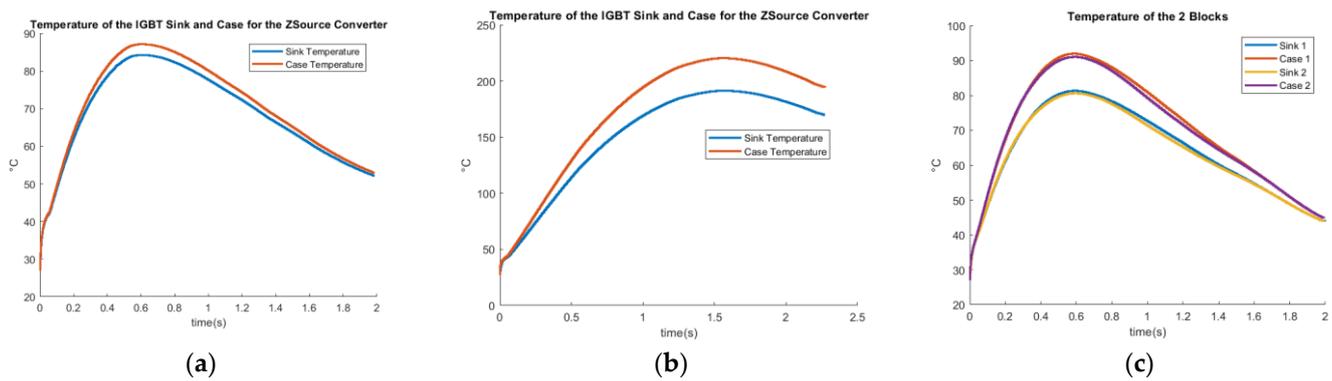


Figure 24. Sink and case temperatures for (a) one IGBT module in the two paralleled ST IGBT module in the BqZSI topology, (b) single ST IGBT module in the BqZSI topology, and (c) the two packages of the 2 modules forming the 3-level DC-DC converter.

The inverter temperatures for the two topologies are presented in Figure 25. The temperature rise for the BqZSI inverter could be of concern for the proper functioning of the inverter modules. This has been expected due to the increased power loss at low speed. The implementation of a forced ventilation system is a necessity in this case. Whereas for the SVPWM-based inverter, the temperature rise is relatively adequate.

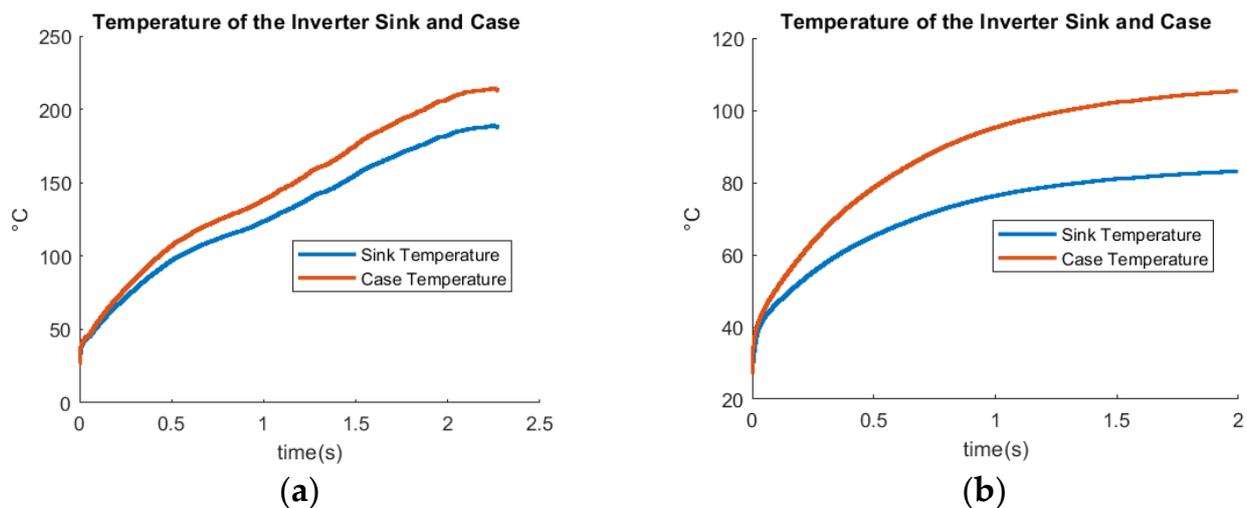


Figure 25. Sink and case temperatures for (a) the inverter module of the BqZSI topology and (b) the inverter module of the 3-level DC/DC converter.

5.4.8. Spectral Analysis

The spectrogram will be used to study the harmonic spectra distribution over time of the UC voltage and the motor torque. Low and high frequencies could have effects on UC ageing. The inverter output voltage analysis is also a cause of increased ion losses (eddy current losses, hysteresis losses), electromagnetic interference, and formation of destabilizing torque. The presence of high frequencies in the BqZSI is noticeable comparing to the 3-level/SVPWM topology (Figure 26) leading to higher influences of the perturbations and losses cited.

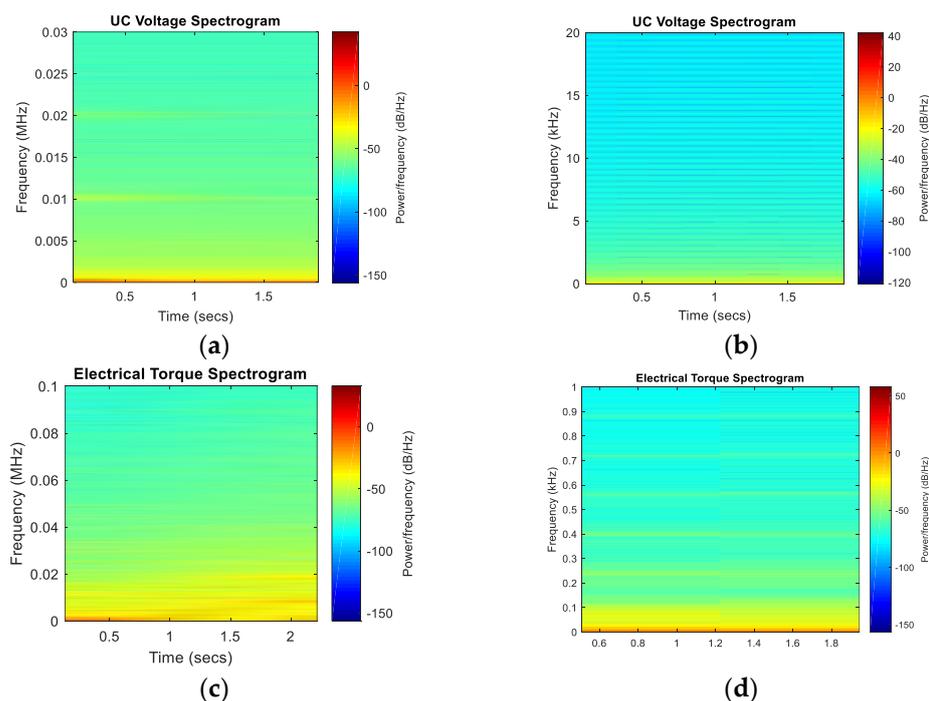


Figure 26. Spectrogram of the UC voltage and motor braking torque of the BqZSI topology (a,c) and for the 3-level/SVPWM topology (b,d).

6. Discussion

A comparison between two semi-active configurations (based on a 3-level converter, and based on a Z-source converter) integrated into an HESS supplying a two-front wheel driven electric vehicle has been proposed. Innovative aspects lead to the fact that we have performed a multicriteria comparison of the two cited power electronic topologies that could be used in EV powertrains, particularly for the management of extreme regenerative braking operations.

A topology of two paralleled IGBT modules is introduced instead of the single shoot-through IGBT module in order to minimize power losses and make the Z-source comparable to the 3-level/SVPWM topology in terms of efficiency and power loss. Open-loop and closed-loop controls were implemented in the Z-source. At first glance, the structural simplicity of Z-source could defy the complexity control of the 3-level DC/DC converter.

The simulation tests were performed during extreme braking maneuvers. This type of braking operation involves a worst-case scenario power flow according to the power converters and energy storage elements. The comparison was based on switches' voltage stresses, power efficiency of the whole conversion powertrain, temperature rise, and spectral analysis of the main quantities. Results showed that the proposed 3-level/SVPWM topology provides better performances in terms of power losses, efficiency, thermal behavior, and electromagnetic interference.

The simulation provided a flexible and configurable toolbox for a software powertrain environment taking into consideration the surface road type, the vehicle sizing, the energy storage types, the power electronics configurations, and all the appropriate control strategies for the different elements constituting the power conversion chain and process.

Authors are trying to improve the overall power transmission and conversion system of the electric vehicle, by considering the hybridization effect of the energy storage system. For that purpose, after validating the adopted power electronic architectures and control methods solutions, the next step is to work on an energetic-based simulation platform. This platform will involve simplified averaged models enabling fast computation of the energetic behavior for standard drive cycles and allowing the implementation of innovative energy management strategies. Second, further work could concern the impact of the ABS

control system on the robustness of the control algorithms and its repercussion on the whole powertrain system.

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